

JAN 05 2006

Attorney's Docket No.: 10559-391001  
Client's Ref. No.: P10256 - ADI APD1812-1-US

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**OFFICIAL FAX NO: (571) 273-8300**

Number of pages including this page 18

Applicant : Ryo Inoue et al.  
Serial No. : 09/745,104  
Filed : December 20, 2000

Art Unit : 2181  
Examiner : Tonia L. Meonske  
Assignee: Intel Corporation and Analog Devices, Inc.

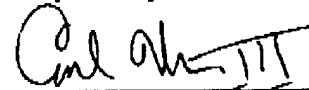
Title : Hardware Loops and A Pipeline System Using Advanced Generation of Loop Parameters  
(As Amended)

**Mail Stop Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet are an Amendment and Transmittal Letter, faxed this 5<sup>th</sup> day of January, 2006, to the United States Patent and Trademark Office.

Date: January 5, 2006

Respectfully submitted,



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